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NSD3604 is a highly integrated device which includes four half bridge (HB2, HB3, HB6, HB7) gate drivers and two current sense amplifiers.

Gate drivers provide advanced functions like slew rate control, switching timing feedback and VGS handshaking. Current sense amplifiers support high common mode voltage input. A 16-bit SPI is used to configure and control device also read out status registers for diagnostic.

Device offers an array of diagnostic features to ensure robust operation. These features include supply voltage monitor, charge pump voltage monitor, VDS overvoltage monitor, VGS voltage monitor and thermal monitor (warning and shutdown protection).

## Y e 9Fea wors

- AEC-Q100Grade1 qualified
- 4halfbridge gate drivers
   4.9V to 37V operating range
   Halfbridge, H bridge and SPn

.25mA to 64mA source/sink current for different phases

Turn on/off delay and rise/fall timing feedback

 Integrated 2-stage charge-pump with spread spectrum

Support 0% to 100% PWMoperation Reverse protection MOSFET driver output

 2 wide common mode current sense amplifier (CSA)

Support high side, low side and inline topology

Configurable gain (10/20/40/80V/V)
Configurable over current threshold, filter time and fault reaction

- Lowcurrent consumption in sleep mode
- 16-bit, max 10MHz SPI interface
- Protection and diagnostic

Supply and regulator voltage monitor (DVDD UV, PVDD OV, PVDD UV and VCP UV) Gate driver monitor (VGS Fault and VDS OV) Support off state diagnostic Thermal warning and shutdown

Mindowwatchdog timer

Window watchdog timer

Support brake function (LS6~LS7) in sleep mode and normal mode

Dedicated driver disable pin (DRVOFF) or Fault interrupt pin (nFLT)

• RoHS & REACH Compliance

### A l.ca. s

- Automotive brushed DC motor applications (Seat, Power lift gate...)
- Automotive body control functions (Door locks, Latch...)

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NSD3604-Q1QAIR	VQFN40	6.0mm × 6.0mm
NSD3604-Q1QAJR	VQFN56	8.0mm × 8.0mm

#### For full version datasheet, please contact: pad\_marketing@novosns.com

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